

INVENTOR INFORMATION

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CORRESPONDENCE INFORMATION

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APPLICATION INFORMATION

Title Line One:: LOW VOLTAGE MOSFET AND PROCESS FOR ITS M
Title Line Two:: ANUFACTURE AND CIRCUIT APPLICATION
Total Drawing Sheets:: 5
Formal Drawings?:: Yes
Application Type:: Utility
Docket Number:: 2-2500
Secrecy Order in Parent Appl.?:: No

CONTINUITY INFORMATION

This application is a:: DIVISION OF
> Application One:: 09/436,302
Filing Date:: 11-08-1999

Which is a:: NON PROV. OF PROVISIONAL
>> Application Two:: 60/107,700
Filing Date:: 11-09-1998

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